



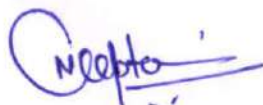
**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. in Electrical and Electronics Engineering**

**SEMESTER - I (2021-2025)**

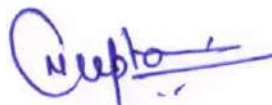
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTMA101	Applied Mathematics I	3	1	0	4	60	20	20	0	0	100
2	BTPH101	Applied Physics	3	1	2	5	60	20	20	30	20	150
3	BTEE102	Fundamentals of Electrical Engineering	3	0	2	4	60	20	20	30	20	150
4	BTME105	Fundamentals of Mechanical Engineering and Applied Mechanics	3	0	2	4	60	20	20	30	20	150
5	BTCS101	Computer Programming I	0	0	2	1	0	0	0	30	20	50
6	HUCS101	Communication Skills	1	0	2	2	60	20	20	0	20	120
7	BTEE105	Basics of MATLAB	0	0	8	4	0	0	0	30	20	50
<b>TOTAL</b>			<b>13</b>	<b>2</b>	<b>18</b>	<b>24</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>150</b>	<b>120</b>	<b>770</b>

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



**Chairperson**  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore



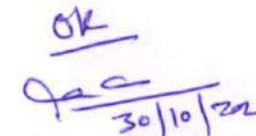
**Chairperson**  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore



**Controller of Examination**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore



**Joint Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore



**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore